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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Leonard Forbes et al.

Title: TRANSISTOR WITH VARIABLE ELECTRON AFFINITY GATE AND METHODS OF FABRICATION AND USE

Docket No.: 303.324US4

Serial No.: 09/691,004

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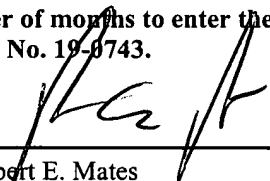
Commissioner for Patents
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- ☒ A return postcard.
- ☒ A Supplemental Information Disclosure Statement (1 pgs.), Form 1449 (1 pgs.), and copies of 3 cited references.

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